

SRI VENKATESWARA COLLEGE OF ENGINEERING (AUTONOMOUS)

Karakambadi Road, Tirupati-517507

EXAMINATION BRANCH

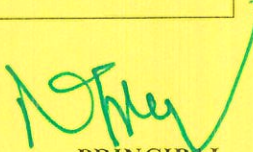
M.Tech I Semester (R20) Regular & Supplementary Examinations April-2024 TIME TABLE

Time: **AN: 12.30 P.M TO 03.30 P.M**

Date/Day	Power Systems (PS)	Advanced Manufacturing Systems (AMS)	VLSI Design (VLSI D)	Computer Science and Engineering (CSE)
08-04-2024 (Monday)	Power Quality EE20DPC101	Modern Machining Processes ME20DPC101	CMOS Analog IC Design EC20DPC101	Mathematical Foundations of Computer Science CS20DPC101
12-04-2024 (Friday)	Advanced Power System Protection EE20DPC102	Automation in Manufacturing ME20DPC102	CMOS Digital IC Design EC20DPC102	Advanced Data Structures CS20DPC102
15-04-2024 (Monday)	Solar Energy Conversion Systems EE20DPE104	Advanced Casting and Welding Technology ME20DPE104	Nanomaterials & Nanotechnology EC20DPE104	Machine Learning CS20DPE103
18-04-2024 (Thursday)	Energy Auditing & Management EE20DPE106	Design for Assembly and Manufacturing ME20DPE108	CPLD and FPGA Architectures and Applications EC20DPE107	Data Science CS20DPE106
20-04-2024 (Saturday)	Research Methodology and IPR EE20DOE109	Research Methodology and IPR ME20DPC111	Research Methodology and IPR EC20DPC109	Research Methodology and IPR CS20DPC109

Note: Any discrepancy in this time table may be brought to the notice of the under signed immediately.


CONTROLLER OF EXAMINATIONS


PRINCIPAL

Date: 27-03-2024